

DIGITAL VLSI CLUB ACTIVITY REPORT



JADAVPUR UNIVERSITY
188 RAJA SUBODH CHANDRA MALLICK ROAD
KOLKATA: - 700032

❖ Activity 1:-

✚ **Name of the Host** :Santanu Mahapatra

✚ **Brochure** :

Interactive Session
with
Prof. Santanu Mahapatra,
Department of Electronic Systems Engineering (formerly CEDT)
Indian Institute of Science (IISc) Bangalore



Prof. Santanu Mahapatra received his B.E. degree from the Dept. of ETCE, Jadavpur University, in 1999, M. Tech. degree in the field of Electrical Engineering (Microelectronics) in 2001 from Indian Institute of Technology (IIT) Kanpur, and Ph.D. degree from Swiss Federal Institute of Technology Lausanne (EPFL) in 2005. His research interests include two dimensional channel transistors, energy efficient electronic switches and energy-storage at nano-scale. He has several sponsored projects and more than 100 journal publications.

Date :10th February, 2023
Time : 2:30 pm
Venue: Seminar room,
Dept. of ETCE, JU



Organizer:
Prof. Sayan Chatterjee
Dr. Chandrima Mondal

IC Design and Fabrication Centre, Jadavpur University
Department of Electronics and Telecommunication Engineering, Jadavpur University

Interested UG/PG students please send a mail to fdp@jadavpuruniversity.in

Attendance Sheet :

Trideb Narayan Roy	BE	2 ETCE
Rishav Roy	BE	4 ETCE
Himanshu Shaw	BE	3 ETCE
Suraj Mohan Soren	BE	4 Instrumentation and electronics engineering
Subha Bhattacharya	BE	2 Electrical engineering
Paulami Nayek	M.TECH	1 ETCE (VLSI)
Goirik Dhar	M.TECH	1 ETCE (VLSI and Micro Electronics)
Sreejita Deb	M.TECH	1 Vlsi and microelectronics
Soumyadeep kar	BE	2 Electronics and telecommunication engineering
Gulshan poddar	BE	4 Instrumentation And Electronics
Soham Pramanik	BE	3 ETCE
Soukarya Biswas	BE	3 Electronics and Telecommunication engineering
Subharthi Ray	BE	2 Electronics and Telecommunication
Sinjita Gayen	BE	2 ELECTRONICS AND TELECOMMUNICATION ENGINEERING
Sayan Laha	BE	2 Electrical Engineering
Debjyoti Pakray	B.TECH	2 ETCE
Rudrajeet Ghosh	M.TECH	2 VLSI and Microelectronics
SAYAN CHATTERJEE	M.TECH	2 VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
RIKTA PAL	M.TECH	2 ETCE (VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Sourin Karmakar	M.TECH	2 ETCE(VLSI)
ANKIT	BE	4 Instrumentation and electronics engineering
Suprotim Datta	M.TECH	1 ETCE(VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Shahbaz Parwez	BE	4 IEE
Sk Shayeed	M.TECH	1 M,tch in VLSI and Microelectronics
Suryadip Mondal	BE	2 Electrical Engineering
Arghyadeep Malakar	BE	3 Electrical Engineering
Manas Sarkar	BE	3 Electrical Engineering
Saheli Ghosh	M.TECH	2 VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
Akash Bose	M.TECH	2 VLSI
Sayar Paul	BE	2 Electrical Engineering
ARKAPRABHA PAL	BE	2 Electronics and Tele-Communication Engineering

✚ Pictures of Lecture :




❖ Activity 2:-

✚ Name of the Host :Amit Roy

✚ Brochure :


The brochure is for a lecture by Mr. Amit Roy, Director of R&D at Synopsys. It is organized by the Department of Electronics and Telecommunication Engineering at Jadavpur University. The lecture is titled "Lecture on EDA Overview Industry Landscape Career" and is scheduled for Friday, 28 April, from 2:30 PM to 4:30 PM. The location is Room T-4-7, Prayukti Bhavan, Jadavpur University. Contact information for details is provided as +919830111930, Prof. Sheli Sinha Chaudhuri.

Department of Electronics
and Telecommunication
Engineering



SYNOPSYS
Silicon to Software™

Mr. Amit Roy
Director, R&D
Synopsys



Lecture on
EDA Overview
Industry Landscape
Career

Friday
28 April
2:30 PM - 4:30 PM

Room T-4-7
Prayukti Bhavan
Jadavpur University

For details contact
+919830111930 Prof. Sheli Sinha Chaudhuri

+ Attendance Sheet :

Soham Pramanik	BE	3	ETCE
Soukarya Biswas	BE	3	Electronics and Telecommunication engineering
Subharthi Ray	BE	2	Electronics and Telecommunication
Sinjita Gayen	BE	2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING
Sayan Laha	BE	2	Electrical Engineering
Debjyoti Pakray	B.TECH	2	ETCE
Rudrajeet Ghosh	M.TECH	2	VLSI and Microelectronics
SAYAN CHATTERJEE	M.TECH	2	VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
RIKTA PAL	M.TECH	2	ETCE (VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Sourin Karmakar	M.TECH	2	ETCE(VLSI)
ANKIT	BE	4	Instrumentation and electronics engineering
Suprotim Datta	M.TECH	1	ETCE(VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Shahbaz Parwez	BE	4	IEE
Sk Shayeed	M.TECH	1	M.tech in VLSI and Microelectronics
Suryadip Mondal	BE	2	Electrical Engineering
Arghyadeep Malakar	BE	3	Electrical Engineering
Manas Sarkar	BE	3	Electrical Engineering
Saheli Ghosh	M.TECH	2	VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
Akash Bose	M.TECH	2	VLSI
Sayar Paul	BE	2	Electrical Engineering
ARKAPRABHA PAL	BE	2	Electronics and Tele-Communication Engineering
Gourav Das	BE	2	Electrical Engineering
Divyansh Dutta	BE	1	Electrical Engineering
Simontini Roy	M.TECH	1	ETCE (Vlsi & Microelectronics)
Bijetri Chakraborty	M.TECH	1	ETCE(Vlsi and Microelectronics)
Amrita Chakrabarty	M.TECH	1	VLSI and Microelectronics
SHAMPA PAUL	M.TECH	1	ETCE (VLSI AND MICROELECTRONICS)
SIDDHARTHA BARUA	M.TECH	2	VLSI Design and Microelectronics Technology
Shirshendu Mandal	BE	2	Electronics
Aditya Bhattacharjee	BE	3	Electronics and telecommunication

✚ Pictures of Lecture :









❖ Activity 3:-

✚ **Name of the Host :Mr. Arindam Mitra**

✚ **Brochure :**



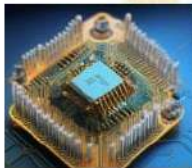
Technical Lecture
on

Ansys

INTRODUCTION TO RTL LEVEL POWER ANALYSIS AND OPTIMIZATION USING POWERARTIST

Mr. Arindam Mitra

Arindam Mitra is working as a Director of Application Engineering at Ansys, based in San Jose, CA. He had done graduation in ETCE in 1999 from Jadavpur University. He has 22+ years of EDA experience starting with Delseoft/Atrenta (in Kolkata, working on SpyGlass), followed by Ansys.



18TH JULY, 2023, 4 PM AT T-3-7

Organized by

IC Design and Fabrication Center

Department of Electronics and Telecommunication Engineering

Jadavpur University, Kolkata , India



Attendance Sheet :

1	Rikta Pal	ETCE PG (VLSI Design and Microelectronics Technology)	
2			
3	2	Adway Paul	ETCE UG 3
4	3	Sayan Chatterjee	ETCE PG (VLSI Design and Microelectronics Technology)
5	4	Rudrajeet Ghosh	ETCE PG (VLSI Design and Microelectronics Technology)
6	5	Subha Bhattacharya	EE UG 2
7	6	Sayan Laha	EE UG 2
8	7	Himanshu Shaw	ETCE UG 3
9	8	Sourin Karmakar	ETCE PG (VLSI Design and Microelectronics Technology)
10	9	Subharthi Ray	ETCE UG 2
11	72	Sadiq Siraj Ebrahim	ETCE UG3
12			
13	12	Akash Bose	ETCE PG (VLSI Design and Microelectronics Technology)
14	13	Ankit	IEE UG4
15			
16	15	Bijetri Chakraborty	ETCE PG (VLSI Design and Microelectronics Technology)
17	16	Debangan Roy Choudhury	ETCE UG 3
18	17	Debjyoti Pakray	ETCE 2nd year
19	18	Goirik Dhar	ETCE PG (VLSI Design and Microelectronics Technology)
20	19	Krishnarjun Mitra	ETCE UG 2
21	20	Mainak Mukherjee	ETCE UG 2
22	21	Nabamita Bakshi	ETCE PG (VLSI Design and Microelectronics Technology)
23	22	Olivia Gupta	ETCE PG (VLSI Design and Microelectronics Technology)
24	23	Palit	EE UG 1
25	24	Paulami Nayek	ETCE PG (VLSI Design and Microelectronics Technology)
26	25	Rajani Das	ETCE PG (VLSI Design and Microelectronics Technology)
27	26	Pulak Majumdar	PhD
28	27	Rathin	PhD

 **Pictures of Lecture :**



❖ Activity 4:-

✚ **Name of the Host :Mr. Biswadeep Chatterjee**

✚ **Brochure :**

First lecture series organized by
Digital VLSI Club
in collaboration with
IC Design and Fabrication Center
Dept. of Electronics and Telecommunication Engineering
Jadavpur University



BISWADEEP CHATTERJEE
Associate Vice President at HCL Technologies
Lecture on
Diverse Career Opportunities that semiconductor industry exposes one to
"What do they know about semiconductor who only semiconductor know"

 **FRIDAY**
5 JANUARY 2024

 **TIME**
3:00 PM

VENUE:- T-3-7

SPEAKER
BISWADEEP
CHATTERJEE

CONTACT
Rikta Pal (Member of Digital VLSI Club,
Jadavpur University)
(9748290812)
Prof. Sayan Chatterjee(Faculty Coordinator)
(9433032909)



Attendance Sheet :

1	Rikta Pal	:TCE PG (VLSI Design and Microelectronics Technology
2	Adway Paul	ETCE UG 3
3	Sayan Chatterjee	:TCE PG (VLSI Design and Microelectronics Technology
4	Rudrajeet Ghosh	:TCE PG (VLSI Design and Microelectronics Technology
5	Subha Bhattacharya	EE UG 2
6	Sayan Laha	EE UG 2
7	Himanshu Shaw	ETCE UG 3
8	Sourin Karmakar	:TCE PG (VLSI Design and Microelectronics Technology
9	Subharthi Ray	ETCE UG 2
10	Sadiq Siraj Ebrahim	ETCE UG 3
11	Akash Bose	:TCE PG (VLSI Design and Microelectronics Technology
12	Ankit	IEE UG 4
13	Bijetri Chakraborty	:TCE PG (VLSI Design and Microelectronics Technology
14	Debangana Roy Choudhury	ETCE UG 3
15	Debjoyoti Pakray	ETCE 2nd year
16	Goirik Dhar	:TCE PG (VLSI Design and Microelectronics Technology
17	Krishnarjun Mitra	ETCE UG 2
18	Mainak Mukherjee	ETCE UG 2
19	Nabamita Bakshi	:TCE PG (VLSI Design and Microelectronics Technology
20	Olivia Gupta	:TCE PG (VLSI Design and Microelectronics Technology
21	Palit	EE UG 1
22	Paulami Nayek	:TCE PG (VLSI Design and Microelectronics Technology
23	Rajani Das	:TCE PG (VLSI Design and Microelectronics Technology
24	Pulak Majumdar	PhD
25	Rathin	PhD
26	Reon	IEE UG 4
27	Saheli Ghosh	:TCE PG (VLSI Design and Microelectronics Technology
28	Shahbaz	IEE UG 4
29	Shampa Paul	:TCE PG (VLSI Design and Microelectronics Technology
30	Shriya	ETCE PG 1
31	Siddhartha Barua	:TCE PG (VLSI Design and Microelectronics Technology
32	Simontini Roy	:TCE PG (VLSI Design and Microelectronics Technology
33	Sinjita Gayen	:TCE PG (VLSI Design and Microelectronics Technology
34	Soham Paramanik	:TCE PG (VLSI Design and Microelectronics Technology
35	Somrita Das	:TCE PG (VLSI Design and Microelectronics Technology
36	Souvik Majumdar	Power Engineering UG 3
37	Sreejita Deb	:TCE PG (VLSI Design and Microelectronics Technology
38	Suchismta Sarkar	ETCE PG 1
39	Sumit Kumar	ETCE
40	Sunetra	:TCE PG (VLSI Design and Microelectronics Technology
41	Suraj Mohan Soren	IEE UG 4
42	Apurba	
43	Arkaprava Pal	
44	Arpan Roy	EE UG 3
45	Arya Pandit	ETCE UG 2
46	Srijan Bhattacharjee	ETCE
47	Aditya Bhattacharjee	ETCE UG 3
48	Apurba Sau	ETCE UG 3
49	Arpan Roy	ETCE

✚ Pictures of Lecture :







❖ Activity 5:-

✚ **Name of the Host :Dr. Shubhankar Basu**

✚ **Brochure :**



✚ **Attendance Sheet :**

Rikta Pal	PG-2	VLSI Design and Microelectronics Technology (ETCE
Rudrajeet Ghosh	PG-2	VLSI Design and Microelectronics Technology (ETCE
Sayan Chatterjee	PG-2	VLSI Design and Microelectronics Technology (ETCE
Adway Paul	UG-3	ETCE
Himanshu Shaw	UG-3	ETCE
Paulami Nayek	PG-1	VLSI Design and Microelectronics Technology (ETCE
Sreejita Deb	PG-1	VLSI Design and Microelectronics Technology (ETCE
Gulshan Poddar	UG-4	IEE
Sadiq Siraj Ebrahim	BE-3	ETCE
Subharthi Ray	UG-2	ETCE
Bijetri Chakraborty	PG-1	VLSI Design and Microelectronics Technology (ETCE
Trideb Narayan Roy	BE-2	ETCE
Rishav Roy	BE-4	ETCE
Sayan Laha	BE-2	Electrical engineering
Goirik Dhar	M.TECH-1	ETCE (VLSI and Micro Electronics)
Soumyadeep kar	BE-2	Electronics and telecommunication engineering
Soham Pramanik	BE-3	ETCE
Ankit	UG-4	IEE
Sinjita Gayen	BE-2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Rikta Pal	PG-2	VLSI Design and Microelectronics Technology (ETCE
Rudrajeet Ghosh	PG-2	VLSI Design and Microelectronics Technology (ETCE
Sayan Chatterjee	PG-2	VLSI Design and Microelectronics Technology (ETCE
Adway Paul	UG-3	ETCE
Himanshu Shaw	UG-3	ETCE
Paulami Nayek	PG-1	VLSI Design and Microelectronics Technology (ETCE
Sreejita Deb	PG-1	VLSI Design and Microelectronics Technology (ETCE
Gulshan Poddar	UG-4	IEE
Sadiq Siraj Ebrahim	BE-3	ETCE
Subharthi Ray	UG-2	ETCE
Bijetri Chakraborty	PG-1	VLSI Design and Microelectronics Technology (ETCE
Trideb Narayan Roy	BE-2	ETCE
Rishav Roy	BE-4	ETCE
Sayan Laha	BE-2	Electrical engineering
Goirik Dhar	M.TECH-1	ETCE (VLSI and Micro Electronics)
Soumyadeep kar	BE-2	Electronics and telecommunication engineering
Soham Pramanik	BE-3	ETCE
Ankit	UG-4	IEE
Sinjita Gayen	BE-2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING

✚ Pictures of Lecture :










❖ Activity 6:-

✚ **Name of the Host** : Sourajeet Roy & Avirup Dasgupta

✚ **Brochure** :

The brochure features a beige background with red decorative elements. At the top left is the Jadavpur University logo. The main text is centered and reads: 'Department of Electronics and Telecommunication Engineering, Jadavpur University. In association with IEEE Solid-State Circuits Society, Kolkata Chapter. Presents TECHNICAL LECTURES ON Emerging Interconnects and Device and Modeling. Thursday 29 February 3:00 PM. Room T-3-7, Prayukti Bhavan, Jadavpur University. For details contact +918240384498, Adway Paul, ETCE UG3'. Two circular portraits of the hosts, Sourajeet Roy and Avirup Dasgupta, are placed on either side of the date and time information.

 **Department of Electronics and Telecommunication Engineering**
Jadavpur University

In association with
IEEE Solid-State Circuits Society
Kolkata Chapter


Presents

TECHNICAL LECTURES


ON

Emerging Interconnects

Device and Modeling

 **Sourajeet Roy**
Dept. of ECE
IIT Roorkee

Thursday
29 February
3:00 PM

 **Avirup Dasgupta**
Dept. of ECE
IIT Roorkee

Room T-3-7
Prayukti Bhavan
Jadavpur University

For details contact
+918240384498 Adway Paul, ETCE UG3

Attendance Sheet :

Rikta Pal	PG-2	VLSI Design and Microelectronics Technology (ETCE
Rudrajeet Ghosh	PG-2	VLSI Design and Microelectronics Technology (ETCE
Sayan Chatterjee	PG-2	VLSI Design and Microelectronics Technology (ETCE
Adway Paul	UG-3	ETCE
Himanshu Shaw	UG-3	ETCE
Paulami Nayek	PG-1	VLSI Design and Microelectronics Technology (ETCE
Sreejita Deb	PG-1	VLSI Design and Microelectronics Technology (ETCE
Gulshan Poddar	UG-4	IEE
Sadiq Siraj Ebrahim	BE-3	ETCE
Subharthi Ray	UG-2	ETCE
Bijetri Chakraborty	PG-1	VLSI Design and Microelectronics Technology (ETCE
Trideb Narayan Roy	BE-2	ETCE
Rishav Roy	BE-4	ETCE
Sayan Laha	BE-2	Electrical engineering
Goirik Dhar	M.TECH-1	ETCE (VLSI and Micro Electronics)
Soumyadeep kar	BE-2	Electronics and telecommunication engineering
Soham Pramanik	BE-3	ETCE
Ankit	UG-4	IEE
Sinjita Gayen	BE-2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING

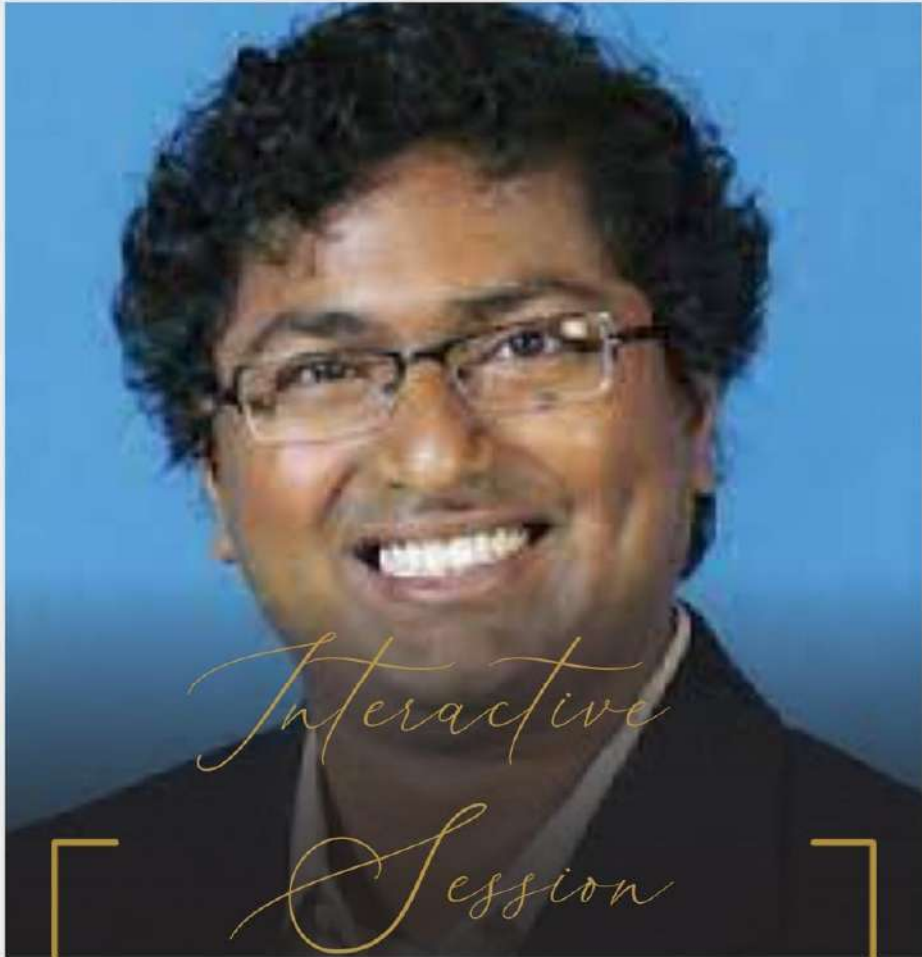
Pictures of Lecture :



❖ Activity 7:-

✚ **Name of the Host :Saibal Mukhopadhyay**

✚ **Brochure :**

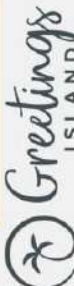


*Interactive
Session*

**SAIBAL MUKHOPADHYAY
GEORGIA TECH**

**4TH JAN, 1PM, T-3-7,
DEPT. OF ETCE, JU**

*Greetings
ISLANDS*



+ Attendance Sheet :

Rikta Pal	PG-2	VLSI Design and Microelectronics Technology (ETCE
Rudrajeet Ghosh	PG-2	VLSI Design and Microelectronics Technology (ETCE
Sayan Chatterjee	PG-2	VLSI Design and Microelectronics Technology (ETCE
Adway Paul	UG-3	ETCE
Himanshu Shaw	UG-3	ETCE
Paulami Nayek	PG-1	VLSI Design and Microelectronics Technology (ETCE
Sreejita Deb	PG-1	VLSI Design and Microelectronics Technology (ETCE
Gulshan Poddar	UG-4	IEE
Sadiq Siraj Ebrahim	BE-3	ETCE
Subharthi Ray	UG-2	ETCE
Bijetri Chakraborty	PG-1	VLSI Design and Microelectronics Technology (ETCE
Trideb Narayan Roy	BE-2	ETCE
Rishav Roy	BE-4	ETCE
Sayan Laha	BE-2	Electrical engineering
Goirik Dhar	M.TECH-1	ETCE (VLSI and Micro Electronics)
Soumyadeep kar	BE-2	Electronics and telecommunication engineering
Soham Pramanik	BE-3	ETCE
Ankit	UG-4	IEE
Sinjita Gayen	BE-2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Soham Pramanik	BE	3	ETCE
Soukarya Biswas	BE	3	Electronics and Telecommunication engineering
Subharthi Ray	BE	2	Electronics and Telecommunication
Sinjita Gayen	BE	2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING
Sayan Laha	BE	2	Electrical Engineering
Debjyoti Pakray	B.TECH	2	ETCE
Rudrajeet Ghosh	M.TECH	2	VLSI and Microelectronics
SAYAN CHATTERJEE	M.TECH	2	VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
RIKTA PAL	M.TECH	2	ETCE (VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Sourin Karmakar	M.TECH	2	ETCE(VLSI)
ANKIT	BE	4	Instrumentation and electronics engineering
Suprotim Datta	M.TECH	1	ETCE(VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Shahbaz Parwez	BE	4	IEE
Sk Shayed	M.TECH	1	M.tech in VLSI and Microelectronics
Suryadip Mondal	BE	2	Electrical Engineering
Arghyadeep Malakar	BE	3	Electrical Engineering
Manas Sarkar	BE	3	Electrical Engineering
Saheli Ghosh	M.TECH	2	VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
Akash Bose	M.TECH	2	VLSI
Sayar Paul	BE	2	Electrical Engineering
ARKAPRABHA PAL	BE	2	Electronics and Tele-Communication Engineering
Gourav Das	BE	2	Electrical Engineering
Divyansh Dutta	BE	1	Electrical Engineering
Simontini Roy	M.TECH	1	ETCE (Vlsi & Microelectronics)
Bijetri Chakraborty	M.TECH	1	ETCE(Vlsi and Microelectronics)
Amrita Chakrabarty	M.TECH	1	VLSI and Microelectronics
SHAMPA PAUL	M.TECH	1	ETCE (VLSI AND MICROELECTRONICS)
SIDDHARTHA BARUA	M.TECH	2	VLSI Design and Microelectronics Technology
Shirshendu Mandal	BE	2	Electronics
Aditya Bhattacharjee	BE	3	Electronics and telecommunication

✚ Pictures of Lecture :





❖ Activity 8:-

✚ **Name of the Host :Pat Hays**

✚ **Brochure :**

Instruction Set Architectures

Pat Hays

Presented to: The Design and Fabrication Centre
Jadavpur University, Kolkata

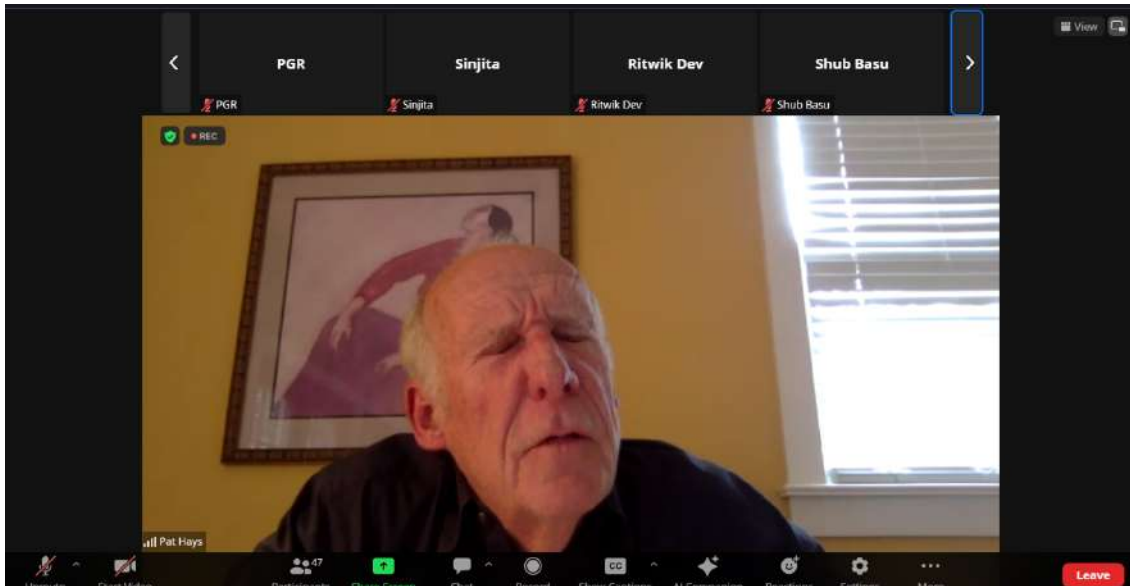
February 17, 2024

+ Attendance Sheet :

Rikta Pal	PG-2	VLSI Design and Microelectronics Technology (ETCE
Rudrajeet Ghosh	PG-2	VLSI Design and Microelectronics Technology (ETCE
Sayan Chatterjee	PG-2	VLSI Design and Microelectronics Technology (ETCE
Adway Paul	UG-3	ETCE
Himanshu Shaw	UG-3	ETCE
Paulami Nayek	PG-1	VLSI Design and Microelectronics Technology (ETCE
Sreejita Deb	PG-1	VLSI Design and Microelectronics Technology (ETCE
Gulshan Poddar	UG-4	IEE
Sadiq Siraj Ebrahim	BE-3	ETCE
Subharthi Ray	UG-2	ETCE
Bijetri Chakraborty	PG-1	VLSI Design and Microelectronics Technology (ETCE
Trideb Narayan Roy	BE-2	ETCE
Rishav Roy	BE-4	ETCE
Sayan Laha	BE-2	Electrical engineering
Goirik Dhar	M.TECH-1	ETCE (VLSI and Micro Electronics)
Soumyadeep kar	BE-2	Electronics and telecommunication engineering
Soham Pramanik	BE-3	ETCE
Ankit	UG-4	IEE
Sinjita Gayen	BE-2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Soham Pramanik	BE	3	ETCE
Soukarya Biswas	BE	3	Electronics and Telecommunication engineering
Subharthi Ray	BE	2	Electronics and Telecommunication
Sinjita Gayen	BE	2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING
Sayan Laha	BE	2	Electrical Engineering
Debjyoti Pakray	B.TECH	2	ETCE
Rudrajeet Ghosh	M.TECH	2	VLSI and Microelectronics
SAYAN CHATTERJEE	M.TECH	2	VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
RIKTA PAL	M.TECH	2	ETCE (VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Sourin Karmakar	M.TECH	2	ETCE(VLSI)
ANKIT	BE	4	Instrumentation and electronics engineering
Suprotim Datta	M.TECH	1	ETCE(VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY)
Shahbaz Parwez	BE	4	IEE
Sk Shayed	M.TECH	1	M.tech in VLSI and Microelectronics
Suryadip Mondal	BE	2	Electrical Engineering
Arghyadeep Malakar	BE	3	Electrical Engineering
Manas Sarkar	BE	3	Electrical Engineering
Saheli Ghosh	M.TECH	2	VLSI DESIGN AND MICROELECTRONICS TECHNOLOGY
Akash Bose	M.TECH	2	VLSI
Sayar Paul	BE	2	Electrical Engineering
ARKAPRABHA PAL	BE	2	Electronics and Tele-Communication Engineering
Gourav Das	BE	2	Electrical Engineering
Divyansh Dutta	BE	1	Electrical Engineering
Simontini Roy	M.TECH	1	ETCE (Vlsi & Microelectronics)
Bijetri Chakraborty	M.TECH	1	ETCE(Vlsi and Microelectronics)
Amrita Chakrabarty	M.TECH	1	VLSI and Microelectronics
SHAMPA PAUL	M.TECH	1	ETCE (VLSI AND MICROELECTRONICS)
SIDDHARTHA BARUA	M.TECH	2	VLSI Design and Microelectronics Technology
Shirshendu Mandal	BE	2	Electronics
Aditya Bhattacharjee	BE	3	Electronics and telecommunication

✚ Pictures of Lecture :



❖ Activity 9:-

✚ **Name of the Host :Dr. Dibyendu Kayal**

✚ **Attendance Sheet :**

Rikta Pal	PG-2	VLSI Design and Microelectronics Technology (ETCE
Rudrajeet Ghosh	PG-2	VLSI Design and Microelectronics Technology (ETCE
Sayan Chatterjee	PG-2	VLSI Design and Microelectronics Technology (ETCE
Adway Paul	UG-3	ETCE
Himanshu Shaw	UG-3	ETCE
Paulami Nayek	PG-1	VLSI Design and Microelectronics Technology (ETCE
Sreejita Deb	PG-1	VLSI Design and Microelectronics Technology (ETCE
Gulshan Poddar	UG-4	IEE
Sadiq Siraj Ebrahim	BE-3	ETCE
Subharthi Ray	UG-2	ETCE
Bijetri Chakraborty	PG-1	VLSI Design and Microelectronics Technology (ETCE
Trideb Narayan Roy	BE-2	ETCE
Rishav Roy	BE-4	ETCE
Sayan Laha	BE-2	Electrical engineering
Goirik Dhar	M.TECH-1	ETCE (VLSI and Micro Electronics)
Soumyadeep kar	BE-2	Electronics and telecommunication engineering
Soham Pramanik	BE-3	ETCE
Ankit	UG-4	IEE
Sinjita Gayen	BE-2	ELECTRONICS AND TELECOMMUNICATION ENGINEERING

✚ **Pictures of Lecture :**





